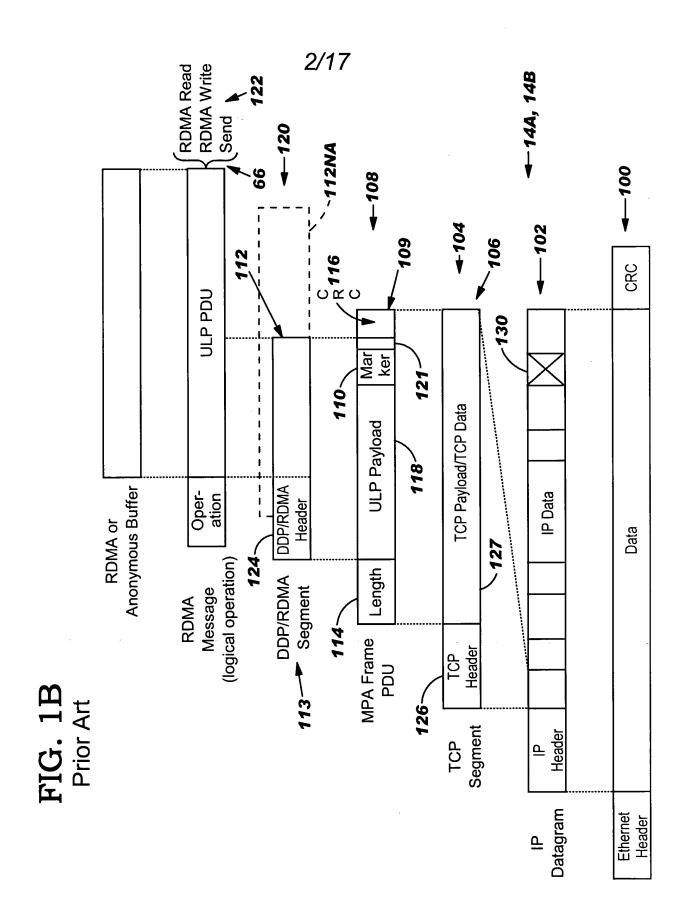
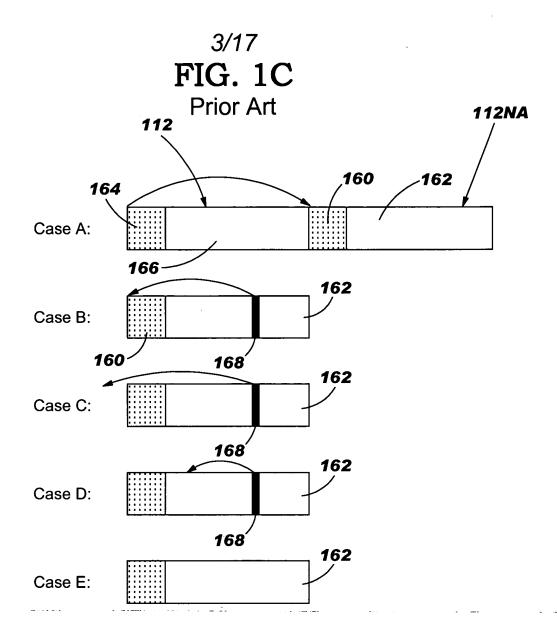
Data Transfer 3B Data Buffers Data Sink 5 User Space Lib. 94 Kernal Module 9B Connection Context Validation Unit Verb Interface **Z** Verb Library 8 FIG. 1A Prior Art Other Component(s) Access Control Reassembly OutLogic Buffers <u>6</u> InLogic RNIC 4 Data Transfer 3A Data Source 2 Data Buffers





Legend:

DDP payload

DDP header

Marker

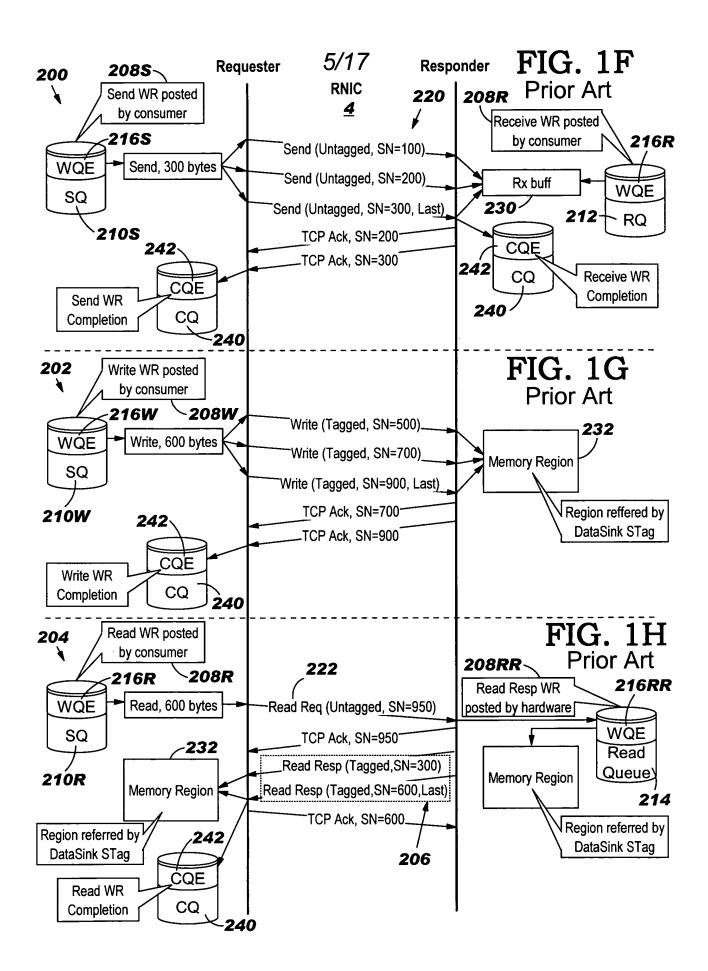
FIG. 1D Prior Art

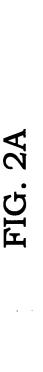
0	1		2			3
0123456	78901234	567	8901	234	45678	901
		TL	Rsvd	DV	RsvdU	LP
		STag				
		TO				
	4					

FIG. 1E

Prior Art

0 1	2		3				
012345678901	12345678901	23	45678901				
	T L Rsvd	DV	RsvdULP[0:7]				
RsvdULP[8:39]							
	QN						
	MSN						
	MO						





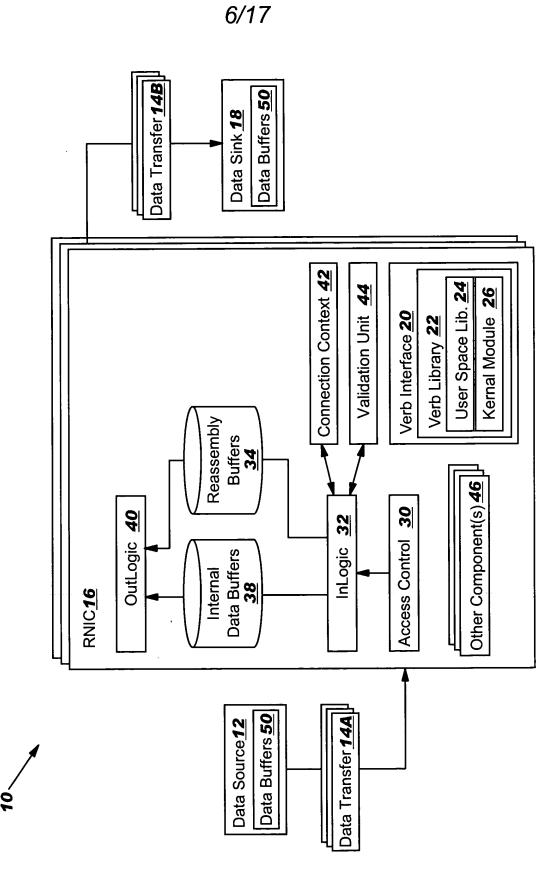


FIG. 2B

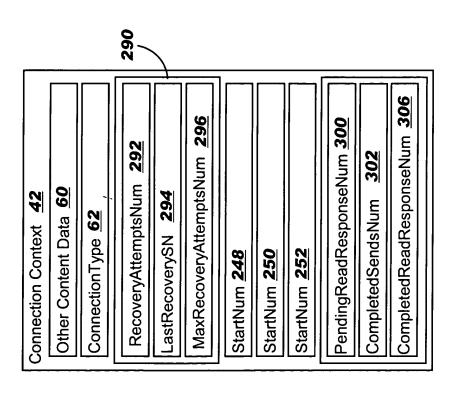
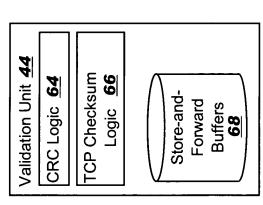
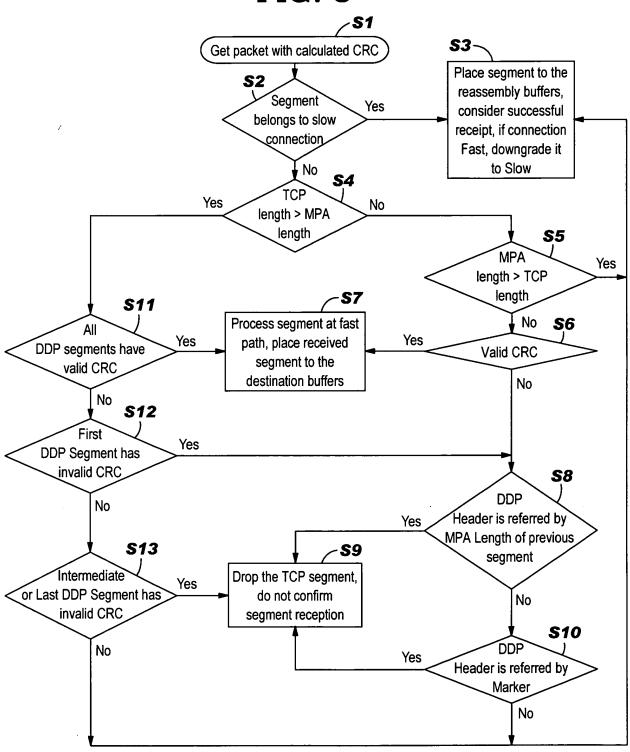


FIG. 2C



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FIG. 3



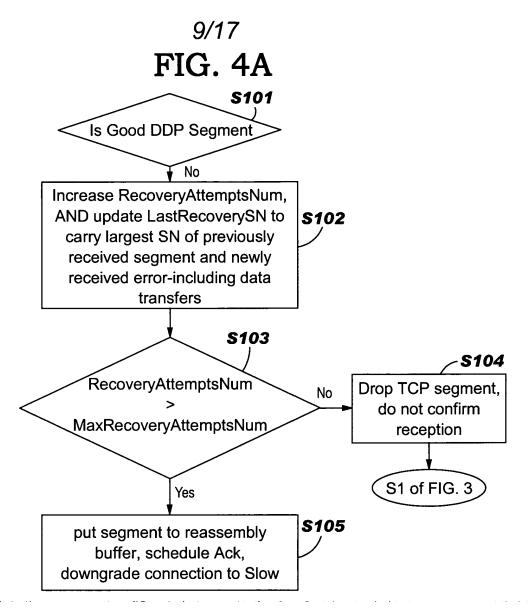


FIG. 4B

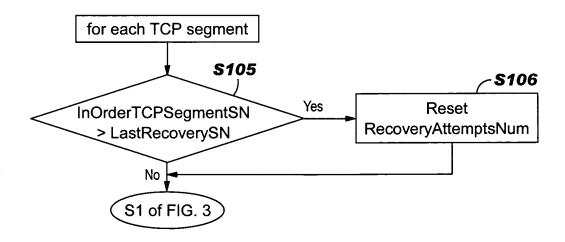
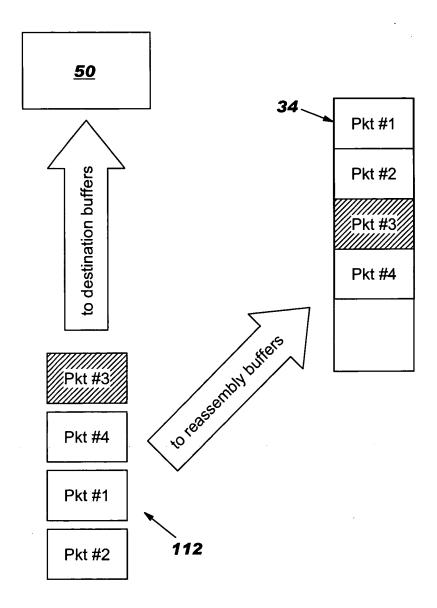


FIG. 5



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FIG. 6

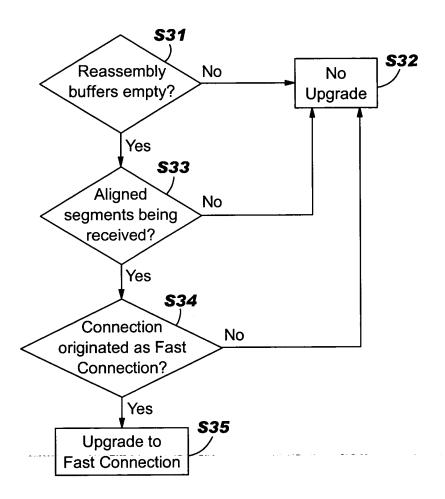


FIG. 7

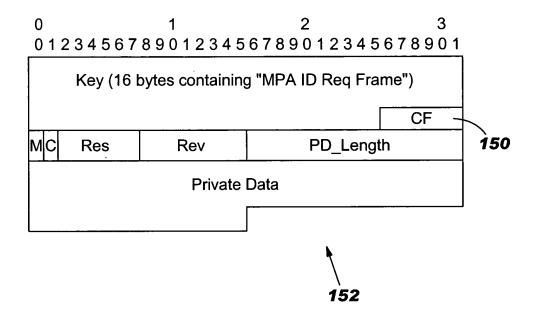
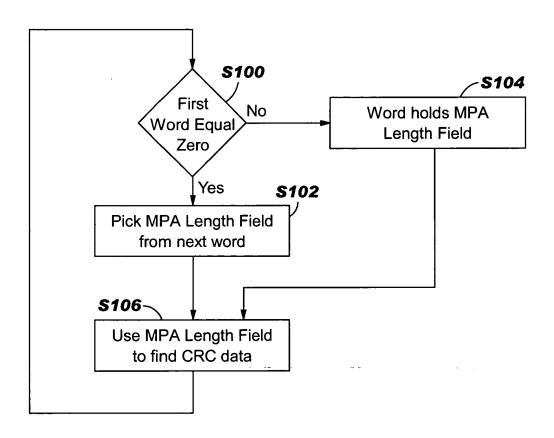
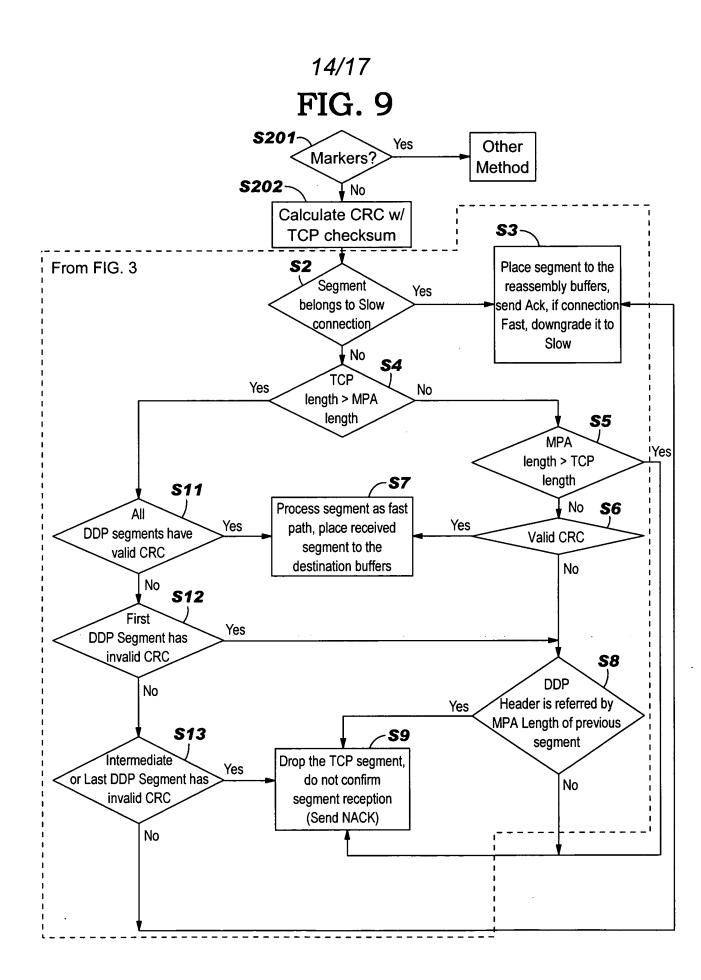


FIG. 8





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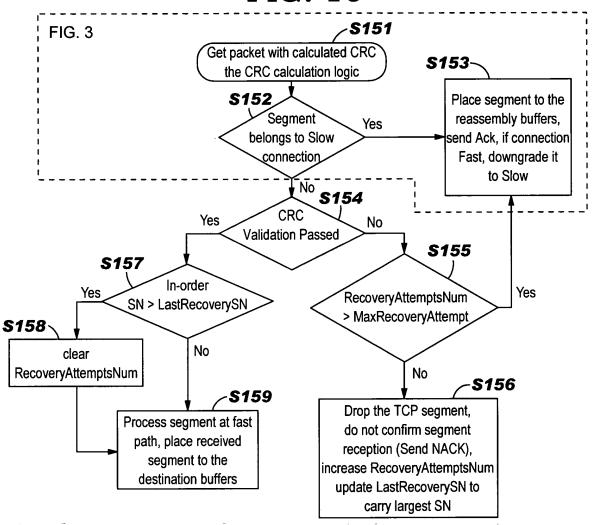
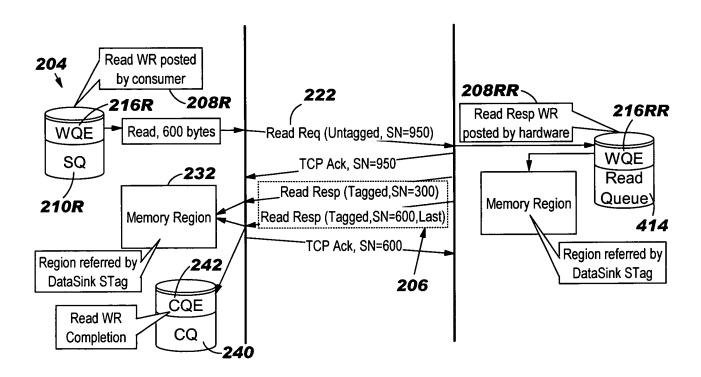


FIG. 11



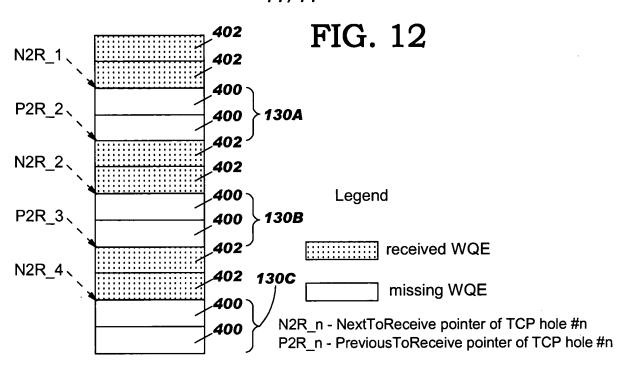


FIG. 13

